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JUL 23 2001
PATENT & TRADEMARK OFFICE

(TO-1449 (Modified) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ATTY. DOCKET NO. RA043D2C3C2	SERIAL NUMBER 09/779,296
	APPLICANT(S) FARMWALD ET AL.	
	FILING DATE FEBRUARY 8, 2001	GROUP ART UNIT 2818

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
TNT	4,570,220	02/11/86	Tetrick et al.	710	126	
	4,099,231	07/04/78	Kotok et al.	711	168	
	5,175,835	12/29/92	Beighe et al.	711	212	
	4,937,734	06/26/90	Bechtolsheim	711	202	
	4,825,416	05/25/89	Tam	365	194	
	5,083,296	01/21/92	Hara et al.	365	239.c2	
	4,792,926	12/20/88	Roberts	365	189.02	
TNT	4,953,128	08/28/90	Kawai et al.	365	194	

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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO	

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TNT	D. Kawley, "SUPERFAST BUS SUPPORTS SUPERFAST TRANSACTIONS", High Performance Systems, pp 90-94 (Sept. 89)
	H. L. Kalter et al. "A 50-ns 16Mb DRAM with a 10-ns Data Rate and On-Chip ECC", IEEE Journal of Solid State Circuits, vol. 25 No. 5, pp. 1118-1128 (Oct 1990)
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	D.T. Wong et. al., "An 11-ns 8Kx18 CMOS Static RAM with 0.5-μm Devices", IEEE Journal of Solid State Circuits, vol. 23 No. 5, pp. 1095-1103 (Oct. 1988)
TNT	T. Williams et. al., "An Experimental 1-Mbit CMOS SRAM with Configurable Organization and Operation", IEEE Journal of Solid State Circuits, vol. 23 No. 5, pp. 1085-1094 (Oct. 1988)

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TNT	4,445,204	04/24/84	Nishiguchi	365	194	
	4,821,226	04/11/89	Christopher et al.	365	230.03	
	4,882,712	11/21/89	Ohno et. al.	365	206	
	4,951,251	08/21/90	Yamaguchi et al.	365	189.02	
	4,928,265	12/29/92	Higuchi ~~Beigbe~~ et al.	365	189.01	
	5,107,465	04/21/92	Fung et al.	365	230.08	
TNT	5,206,833	04/27/93	Lee	365	233	
	~~4,953,128~~	~~08/28/90~~	~~Kawai et al.~~			

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TNT	M. Kimoto, "A 1.4ns/64kb RAM with 85ps/3680 Logic Gate Array", 1989 IEEE Custom Integrated Circuits Conference

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PTO/SB/08A (Modified)	ATTY. DOCKET NO. RA043D2C3C2	SERIAL NUMBER 09/779,296
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EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
TNT	5,140,688	08/18/92	White et al.	713	600	
|	5,018,111	05/21/91	Madland	365	233	
|	4,734,880	03/29/88	Collins	711	105	
|	4,183,095	01/08/80	Ward	365	189.02	
|	4,975,872	12/04/90	Zaiki	365	49	
|	5,016,226	05/14/91	Hiwada et al.	365	233	
TNT	5,109,498	04/28/92	Kamiya et al.	711	123	

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TNT	4,205,373	May 27, 1980	Shah et al.	780	128	
|	4,845,670	Jul. 4, 1989	Nishimoto et al.	365	78	
|	4,509,142	Apr. 2, 1985	Childers	711	169	
TNT	4,685,088	Aug. 4, 1987	Ianucci	365	189.02	

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TNT	0 246 767	April 28, 1987	EPO	—			
TNT	0 334 552	Mar. 16, 1989	EPO	—			
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|	European Search Report for EPO Patent Application No. 89 30 2613
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|	J. Chun et al., "A 1.2ns GaAs 4K Read Only Memory", IEEE Gallium Arsenide Integrated Circuit Symposium Technical Digest, pp. 83-86, (Nov. 1988)
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TNT	4,945,516	07/31/90	Kashiyama	365	189.05	
	4,807,189	02/21/89	Pinkham et al.	365	189.05	
	4,586,167	04/21/89	Fujishima et al.	365	189.05	
	4,337,523	06/29/82	Hotta et al.	365	233	
	4,920,483	04/24/90	Pogue et al.	711	219	
	4,845,664	07/04/89	Aichelmann, Jr. et al.	711	105	
	4,750,839	06/14/88	Wang et al.	365	233	
TNT	4,513,370	04/23/85	Ziv et al.	709	225	
~~—~~	~~4,975,872~~	~~12/04/90~~	~~Zaiki~~			
~~—~~	~~5,109,498~~	~~04/28/92~~	~~Kamiya et al.~~			

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TNT	4,891,791	01/02/90	Iijima	365	189.01	
	4,092,665	05/30/78	Saran	341	63	
	4,799,199	01/17/89	Scales, III et al.	365	230.08	
	5,093,807	03/03/92	Hashimoto et al.	365	230.09	
	3,882,470	05/06/75	Hunter	365	200	
	4,315,308	02/09/82	Jackson	710	33	
	5,148,523	09/15/92	Harlin et al.	345	519	
	5,142,637	09/25/92	Harlin et al.	711	106	
	4,719,602	01/12/88	Hag et al.	365	189.05	
	4,785,394	11,15,88	Fischer	710	114	
	5,083,260	01/21/92	Tsuchiya	710	113	
	4,954,987	09/04/90	Auvinen et al.	365	189.02	
	4,675,850	06/23/87	Kumanoya et al.	365	230.01	
TNT	4,788,667	11/29/88	Nakano et al.	365	193	

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EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
TNT	4,663,735	05/05/87	Novak, et. al	345	533	
	4,672,470	06/09/87	Morimoto, et. al	386	16	
	4,719,505	01/12/88	Katznelson	348	502	
	4,825,287	04/25/89	Baji, et. al	348	720	
	4,845,677	07/04/89	Chappell, et. al	365	189.02	
	4,873,671	10/10/89	Kowshik, et. al	365	189.12	
	4,876,670	10/24/89	Nakabayashi, et. al	365	194	
TNT	4,901,036	02/13/90	Herold, et. al	331	25	

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TNT	EP 0218523	05/30/89	EPO				
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TNT	Horowitz et al., "**MIPS-X: A 20-MIPS Peak 32-Bit Microprocessor with ON-Chip Cache**", IEEE J. Solid State Circuits, vol. SC-22, No. 5, pp. 790-798 (Oct. 1987)
TNT	Robert J. Lodi et al., "**Chip and System Characteristics of a 2048-Bit MNOS-BORAM LSI Circuit,**" 1976 IEEE International Solid-State Circuits Conference (Feb. 18, 1976)

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TNT	4,979,145	12/18/90	Remington, et. al	711	106	
	5,009,481	04/23/91	Kinoshita, et. al	385	33	
	5,016,226	05/14/91	Hiwada, et. al	365	233	
	5,036,495	07/30/91	Busch, et. al	365	233	
	5,111,486	05/05/92	Oliboni, et. al	375	376	
	5,123,100	06/16/92	Hisada, et. al	711	401	
	5,142,376	08/25/92	Ogura	386	29	
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TNT	Whiteside, Frank, "**A Dual-Port 65ns 64Kx4 DRAM with a 50MHz Serial Output,**" IEEE International Solid-State Circuits Conference Digest (Feb. 1986)

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	5,361,277	11/01/94	Grover	375	356	
TNT	5,684,753	11/04/97	Hashimoto, et al	365	233	

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	4,703,418	Oct. 27, 1987	James	710	32	
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	4,870,562	Sept. 26, 1989	Kimoto et al.	711	167	
	4,763,249	Aug. 09, 1988	Bomba et al	713	600	
	4,394,753	July 19, 1983	Penzel	365	236	
	4,785,428	Nov. 15, 1988	Bajwa	365	185.02	
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TNT	S56-82961	July 7, 1981	Japan				
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	S61-72350	April 14, 1986	Japan				
	S63-142445	June 14, 1988	Japan				
	B63-46864	Sept. 19, 1988	Japan				
TNT	S64-29951	Jan. 31, 1989	Japan				

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	4,466,127	08/14/84	Ohgishi, et. al	455	182.1	
	4,536,795	08/20/85	Hirota, et. al	348	714	
	4,616,268	10/07/86	Shida, et. al	358	451	
	4,629,909	12/16/86	Cameron	327	211	
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TNT	4,482,999	11/13/84	Janson et al.	370	452	
	4,835,674	05/30/89	Collins et al.	709	214	
	5,193,193	03/09/93	Iyer	710	117	
	5,179,667	01/12/93	Iyer	711	167	
	4,926,385	05/15/90	Fujishima et al.	365	230.03	
	4,566,099	01/21/86	Magerl	370	509	
	4,803,621	02/07/89	Kelly	711	5	
	4,589,108	05/13/86	Billy	370	503	
	4,870,622	09/26/89	Aria et al.	365	230.02	
	5,134,699	07/28/92	Aria et al.	710	35	
	4,878,166	10/31/89	Johnson et al.	710	127	
	4,849,965	07/18/89	Chomel et al.	370	438	
TNT	4,851,990	07/25/89	Johnson et al.	710	100	

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PTO-1449 (Modified)	ATTY. DOCKET NO. RA043D2C3C2	SERIAL NUMBER 09/779,296
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	APPLICANT(S) FARMWALD ET AL.	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	FILING DATE FEBRUARY 8, 2001	GROUP ART UNIT 2818

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	4,435,762	03/06/84	Milligan et al.	710	6	
	4,839,801	06/13/89	Nicely et al.	710	35	
	4,949,301	08/14/90	Joshi et al.	711	100	
	4,047,246	09/06/77	Kerllenevich et al.	710	61	
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|	SHO 63-34795	Feb. 15, 1988	Japan				
|	SHO 61-107453	May 26, 1986	Japan				
|	SHO 63-91766	April 22, 1988	Japan				
|	SHO 62-16289	Jan. 24, 1987	Japan				
TNT	SHO 61-160556	Oct. 4, 1986	Japan				

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